Memory glue logic Truth-Table

Logic for Chip Selection pin on Memory DIP

R*	W*	A0-A10	A11	A12	A13	A14	A15	R/W*, WE*, W*	E*, CE*, CS*	OE*

2k x 8-bit + 2k x 8-bit ⇒ 4kBytes (0x2000 – 0x2FFF) 2 DIPs

{4k x 1-bit} x 16 ⇒ 8kByte (0x2000 – 0x3FFF) 16 DIPs, but only wire up 4: 2 starting at 0x2000 and 2 starting at 0x3000